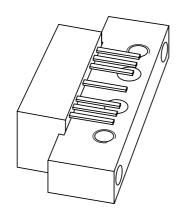
DISCRETE SEMICONDUCTORS

DATA SHEET



BGD804 860 MHz, 20 dB gain power doubler amplifier

Product specification Supersedes data of 1999 Mar 26 2001 Nov 01





Philips Semiconductors

860 MHz, 20 dB gain power doubler amplifier

BGD804

FEATURES

- · Excellent linearity
- · Extremely low noise
- Silicon nitride passivation
- · Rugged construction
- Gold metallization ensures excellent reliability.

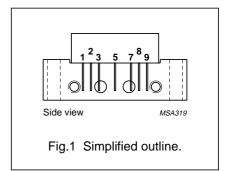
APPLICATIONS

CATV systems in the 40 to 860 MHz frequency range.

PINNING - SOT115J

PIN	DESCRIPTION
1	input
2	common
3	common
5	+V _B
7	common
8	common
9	output

PIN CONFIGURATION



DESCRIPTION

Hybrid amplifier module in a SOT115J package operating at a voltage supply of 24 V (DC).

QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
G _p	power gain	f = 50 MHz	19.5	20.5	dB
		f = 860 MHz	20	_	dB
I _{tot}	total current consumption (DC)	V _B = 24 V	_	410	mA

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER		MAX.	UNIT
Vi	RF input voltage		65	dBmV
T _{stg}	storage temperature		+100	°C
T _{mb}	operating mounting base temperature		+100	°C
V _B	supply voltage	_	25	V

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CHARACTERISTICS

Table 1 Bandwidth 40 to 860 MHz; V_B = 24 V; T_{case} = 35 °C; Z_S = Z_L = 75 Ω

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Gp	power gain	f = 50 MHz	19.5	20	20.5	dB
		f = 860 MHz	20	21	_	dB
SL	slope cable equivalent	f = 40 to 860 MHz	0.2	1.1	2	dB
FL	flatness of frequency response	f = 40 to 860 MHz	_	±0.2	±0.5	dB
S ₁₁	input return losses	f = 40 to 80 MHz	20	28	_	dB
		f = 80 to 160 MHz	18.5	23	_	dB
		f = 160 to 320 MHz	17	20	_	dB
		f = 320 to 640 MHz	15.5	20	_	dB
		f = 640 to 860 MHz	14	20	_	dB
S ₂₂	output return losses	f = 40 to 80 MHz	20	28.5	_	dB
		f = 80 to 160 MHz	18.5	28	_	dB
		f = 160 to 320 MHz	17	24	_	dB
		f = 320 to 640 MHz	15.5	19	_	dB
		f = 640 to 860 MHz	14	19	_	dB
S ₂₁	phase response	f = 50 MHz	-45	_	+45	deg
СТВ	composite triple beat	49 channels flat; V _o = 47 dBmV; measured at 859.25 MHz	_	-64	-61	dB
X _{mod}	cross modulation	49 channels flat; V _o = 47 dBmV; measured at 55.25 MHz	_	-65.5	-62	dB
CSO	composite second order distortion	49 channels flat; V _o = 47 dBmV; measured at 860.5 MHz	-	-63	-58	dB
d ₂	second order distortion	note 1	_	-73	-67	dB
Vo	output voltage	d _{im} = -60 dB; note 2	+60	-61.5	_	dBmV
F	noise figure	f = 50 MHz	_	4.5	5	dB
		f = 550 MHz	_	-	6	dB
		f = 650 MHz	_	-	6	dB
		f = 750 MHz	_	-	6.5	dB
		f = 860 MHz	_	6.5	7.5	dB
I _{tot}	total current consumption (DC)	note 3	_	395	410	mA

Notes

```
1. f_p = 55.25 MHz; V_p = 44 dBmV; f_q = 805.25 MHz; V_q = 44 dBmV; measured at f_p + f_q = 860.5 MHz.
```

2. Measured according to DIN45004B;

```
\begin{split} f_p &= 851.25 \text{ MHz; } V_p = V_o; \\ f_q &= 858.25 \text{ MHz; } V_q = V_o - 6 \text{ dB;} \\ f_r &= 860.25 \text{ MHz; } V_r = V_o - 6 \text{ dB;} \\ \text{measured at } f_p + f_q - f_r = 849.25 \text{ MHz.} \end{split}
```

3. The module normally operates at V_B = 24 V, but is able to withstand supply transients up to 30 V.

860 MHz, 20 dB gain power doubler amplifier

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Table 2 Bandwidth 40 to 860 MHz; $V_B = 24$ V; $T_{case} = 35$ °C; $Z_S = Z_L = 75$ Ω

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Gp	power gain	f = 50 MHz	19.5	20	20.5	dB
		f = 860 MHz	20	21	_	dB
SL	slope cable equivalent	f = 40 to 860 MHz	0.2	1.1	2	dB
FL	flatness of frequency response	f = 40 to 860 MHz	_	±0.2	±0.5	dB
S ₁₁	input return losses	f = 40 to 80 MHz	20	28	_	dB
		f = 80 to 160 MHz	18.5	23	_	dB
		f = 160 to 320 MHz	17	20	_	dB
		f = 320 to 640 MHz	15.5	20	_	dB
		f = 640 to 860 MHz	14	20	_	dB
S ₂₂	output return losses	f = 40 to 80 MHz	20	28.5	_	dB
		f = 80 to 160 MHz	18.5	28	_	dB
		f = 160 to 320 MHz	17	24	_	dB
		f = 320 to 640 MHz	15.5	19	_	dB
		f = 640 to 860 MHz	14	19	_	dB
S ₂₁	phase response	f = 50 MHz	-45	_	+45	deg
СТВ	composite triple beat	129 channels flat; V _o = 44 dBmV; measured at 859.25 MHz	_	-54	-53	dB
X _{mod}	cross modulation	129 channels flat; V _o = 44 dBmV; measured at 55.25 MHz	_	-62	-61	dB
CSO	composite second order distortion	129 channels flat; V _o = 44 dBmV; measured at 860.5 MHz	_	-60.5	-54	dB
d ₂	second order distortion	note 1	_	-73	-67	dB
Vo	output voltage	d _{im} = -60 dB; note 2	+60	-61.5	_	dBmV
F	noise figure	see Table 1	_	_	_	dB
I _{tot}	total current consumption (DC)	note 3	_	395	410	mA

Notes

```
1. f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV};

f_q = 805.25 \text{ MHz}; V_q = 44 \text{ dBmV};

measured at f_p + f_q = 860.5 \text{ MHz}.
```

2. Measured according to DIN45004B;

```
\begin{split} f_p &= 851.25 \text{ MHz; } V_p = V_o; \\ f_q &= 858.25 \text{ MHz; } V_q = V_o - 6 \text{ dB;} \\ f_r &= 860.25 \text{ MHz; } V_r = V_o - 6 \text{ dB;} \\ \text{measured at } f_p + f_q - f_r = 849.25 \text{ MHz.} \end{split}
```

3. The module normally operates at V_B = 24 V, but is able to withstand supply transients up to 30 V.

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Table 3 Bandwidth 40 to 750 MHz; $V_B = 24 \text{ V}$; $T_{case} = 35 \,^{\circ}\text{C}$; $Z_S = Z_L = 75 \,\Omega$

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G _p	power gain	f = 50 MHz	19.5	20	20.5	dB
		f = 750 MHz	20	20.8	_	dB
SL	slope cable equivalent	f = 40 to 750 MHz	0.2	_	2	dB
FL	flatness of frequency response	f = 40 to 750 MHz	_	_	±0.45	dB
S ₁₁	input return losses	f = 40 to 80 MHz	20	28	_	dB
		f = 80 to 160 MHz	18.5	23	_	dB
		f = 160 to 320 MHz	17	20	_	dB
		f = 320 to 640 MHz	15.5	20	_	dB
		f = 640 to 750 MHz	14	20	_	dB
S ₂₂	output return losses	f = 40 to 80 MHz	20	28.5	_	dB
		f = 80 to 160 MHz	18.5	28	_	dB
		f = 160 to 320 MHz	17	24	_	dB
		f = 320 to 640 MHz	15.5	19	_	dB
		f = 640 to 750 MHz	14	19	_	dB
S ₂₁	phase response	f = 50 MHz	-45	_	+45	deg
СТВ	composite triple beat	110 channels flat; V _o = 44 dBmV; measured at 745.25 MHz	_	-59	-57	dB
X _{mod}	cross modulation	110 channels flat; V _o = 44 dBmV; measured at 55.25 MHz	_	-64	-62	dB
CSO	composite second order distortion	110 channels flat; V _o = 44 dBmV; measured at 746.5 MHz	_	-62	-56	dB
d ₂	second order distortion	note 1	_	_	-68	dB
Vo	output voltage	d _{im} = -60 dB; note 2	63	-	-	dBmV
F	noise figure	see Table 1	_	_	_	dB
I _{tot}	total current consumption (DC)	note 3	_	395	410	mA

Notes

```
1. f_p = 55.25 \text{ MHz}; V_p = 44 \text{ dBmV};

f_q = 691.25 \text{ MHz}; V_q = 44 \text{ dBmV};

measured at f_p + f_q = 746.5 \text{ MHz}.
```

2. Measured according to DIN45004B;

```
\begin{split} f_p &= 740.25 \text{ MHz; } V_p = V_o; \\ f_q &= 747.25 \text{ MHz; } V_q = V_o - 6 \text{ dB;} \\ f_r &= 749.25 \text{ MHz; } V_r = V_o - 6 \text{ dB;} \\ \text{measured at } f_p + f_q - f_r = 738.25 \text{ MHz.} \end{split}
```

3. The module normally operates at V_B = 24 V, but is able to withstand supply transients up to 30 V.

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Table 4 Bandwidth 40 to 650 MHz; $V_B = 24 \text{ V}$; $T_{case} = 35 \,^{\circ}\text{C}$; $Z_S = Z_L = 75 \,\Omega$

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G _p	power gain	f = 50 MHz	19.5	20	20.5	dB
		f = 650 MHz	20	20.7	_	dB
SL	slope cable equivalent	f = 40 to 650 MHz	0.2	_	2	dB
FL	flatness of frequency response	f = 40 to 650 MHz	_	_	±0.35	dB
S ₁₁	input return losses	f = 40 to 80 MHz	20	28	_	dB
		f = 80 to 160 MHz	18.5	23	_	dB
		f = 160 to 320 MHz	17	20	_	dB
		f = 320 to 650 MHz	15	20	_	dB
S ₂₂	output return losses	f = 40 to 80 MHz	20	28.5	_	dB
		f = 80 to 160 MHz	18.5	28	_	dB
		f = 160 to 320 MHz	17	24	_	dB
		f = 320 to 650 MHz	15	19	_	dB
S ₂₁	phase response	f = 50 MHz	-45	_	+45	deg
СТВ	composite triple beat	94 channels flat; V _o = 44 dBmV; measured at 649.25 MHz	_	_	-60	dB
X _{mod}	cross modulation	94 channels flat; V _o = 44 dBmV; measured at 55.25 MHz	_	_	-62	dB
CSO	composite second order distortion	94 channels flat; V _o = 44 dBmV; measured at 650.5 MHz	_	_	-58	dB
d ₂	second order distortion	note 1	_	-	-69	dB
Vo	output voltage	d _{im} = -60 dB; note 2	65	_	_	dBmV
F	noise figure	see Table 1	_	_	_	dB
I _{tot}	total current consumption (DC)	note 3	_	395	410	mA

Notes

```
1. f_p = 55.25 MHz; V_p = 44 dBmV; f_q = 595.25 MHz; V_q = 44 dBmV; measured at f_p + f_q = 650.5 MHz.
```

2. Measured according to DIN45004B;

```
\begin{split} f_p &= 640.25 \text{ MHz; } V_p = V_o; \\ f_q &= 647.25 \text{ MHz; } V_q = V_o - 6 \text{ dB;} \\ f_r &= 649.25 \text{ MHz; } V_r = V_o - 6 \text{ dB;} \\ \text{measured at } f_p + f_q - f_r = 638.25 \text{ MHz.} \end{split}
```

3. The module normally operates at $V_B = 24 \text{ V}$, but is able to withstand supply transients up to 30 V.

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Table 5 Bandwidth 40 to 550 MHz; $V_B = 24 \text{ V}$; $T_{case} = 35 \,^{\circ}\text{C}$; $Z_S = Z_L = 75 \,\Omega$

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
G _p	power gain	f = 50 MHz	19.5	20	20.5	dB
		f = 550 MHz	20	20.6	_	dB
SL	slope cable equivalent	f = 40 to 550 MHz	0.2	_	2	dB
FL	flatness of frequency response	f = 40 to 550 MHz	_	_	±0.35	dB
S ₁₁	input return losses	f = 40 to 80 MHz	20	28	_	dB
		f = 80 to 160 MHz	18.5	23	_	dB
		f = 160 to 320 MHz	17	20	_	dB
		f = 320 to 550 MHz	16	20	_	dB
S ₂₂	output return losses	f = 40 to 80 MHz	20	28.5	_	dB
		f = 80 to 160 MHz	18.5	28	_	dB
		f = 160 to 320 MHz	17	24	_	dB
		f = 320 to 550 MHz	16	19	_	dB
S ₂₁	phase response	f = 50 MHz	-45	_	+45	deg
СТВ	composite triple beat	77 channels flat; V _o = 44 dBmV; measured at 547.25 MHz	_	-66	-64	dB
X _{mod}	cross modulation	77 channels flat; V _o = 44 dBmV; measured at 55.25 MHz	_	-67	-64	dB
CSO	composite second order distortion	77 channels flat; V _o = 44 dBmV; measured at 548.5 MHz	_	-67	-62	dB
d ₂	second order distortion	note 1	_	-	-72	dB
Vo	output voltage	d _{im} = -60 dB; note 2	66	_	_	dBmV
F	noise figure	see Table 1	_	_	_	dB
I _{tot}	total current consumption (DC)	note 3	_	395	410	mA

Notes

```
1. f_p = 55.25 MHz; V_p = 44 dBmV; f_q = 493.25 MHz; V_q = 44 dBmV; measured at f_p + f_q = 548.5 MHz.
```

2. Measured according to DIN45004B;

```
\begin{split} f_p &= 540.25 \text{ MHz; } V_p = V_o; \\ f_q &= 547.25 \text{ MHz; } V_q = V_o - 6 \text{ dB;} \\ f_r &= 549.25 \text{ MHz; } V_r = V_o - 6 \text{ dB;} \\ \text{measured at } f_p + f_q - f_r = 538.25 \text{ MHz.} \end{split}
```

3. The module normally operates at $V_B = 24 \text{ V}$, but is able to withstand supply transients up to 30 V.

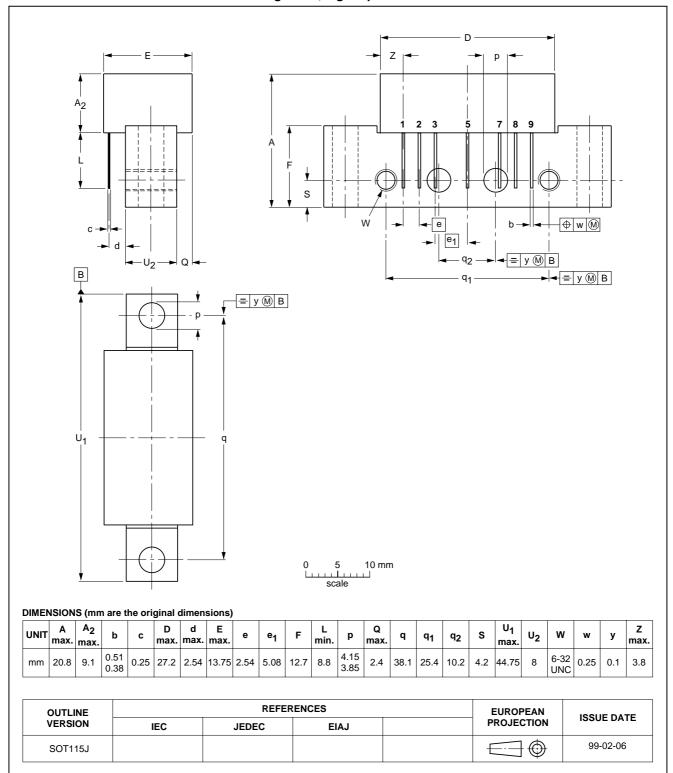
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PACKAGE OUTLINE

Rectangular single-ended package; aluminium flange; 2 vertical mounting holes; 2 x 6-32 UNC and 2 extra horizontal mounting holes; 7 gold-plated in-line leads

SOT115J



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DATA SHEET STATUS

DATA SHEET STATUS(1)	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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NOTES

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NOTES

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